PAT-NO:

JP402076427A

DOCUMENT-IDENTIFIER: JP 02076427 A

TITLE:

INFORMATION TRANSMITTING PROCESSOR

PUBN-DATE:

March 15, 1990

INVENTOR-INFORMATION:

NAME

NAGAO, MASATORA

ASSIGNEE-INFORMATION:

NAME

COUNTRY

TOKYO ELECTRIC CO LTD

N/A

APPL-NO:

JP63229525

APPL-DATE:

September 13, 1988

INT-CL (IPC): H04L001/16, H04L013/08, H04L029/08

US-CL-CURRENT: **714/746**

ABSTRACT:

PURPOSE: To rapidly detect and correct an error by transmitting an error

message including contents specifying the position of the error to

terminating side.

CONSTITUTION: Information transmitted from the terminating side is stored in

a receiving buffer 51. A control part 10 reads out the information stored in

the buffer 51 successively from its head in each byte, stores the number of

units of the read information in a counter 54 and discriminates whether an

error exists in the read information or not. At the existence of an error, an

error message including the contents of the counter 54 is formed in a transmission buffer 52 and then the contents of the buffer 52 are

transmitted

to the terminating side. Thereby, the terminating side can know the byte

number of the byte including the error counted from the head of the information

from the contents of the counter included in the error message, so that the

error can be rapidly detected and corrected.

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